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RESUME

Mrs. Sangeetha Amit Kumar Flat no. 201, City tower building 1, Opp. Al Mahatta park, Al qasimia area, Sharjah, UAE Email ID: sangsvenkat@gmail.com

Objective: Highly motivated, self-driven individual with multi-disciplinary experience in IPR and Teaching with an overall experience of 11+ years. Around 4 years experience in various areas of Patent Preparation and Prosecution and mentoring, around 2 years experience in pre filing searches. Seeking to leverage my skills on different aspects of IPR.

Professional Experience

Consultant, Bay Area IP services, USA	Since August 2022
Senior Patent Analyst, Maxval IP services, Chennai (Pre-filing searches)	August 2020- March 2022.
Women Scientist 'C' under Kiran IPR scheme of TIFAC	Aug 2019-July 2020
BIT (Mesra) Offshore Campus Ras Al Kaimah	From Oct 2011 to August 201

Assistant Professor in the department of Electronics and Communication Teaching courses in Analog communication, Mobile and Cellular communication, Control Theory, Data communication, Digital Logic Design, Basic Electronics and instructor for Basic Electronics Lab, Communication Lab, Digital Logic Design Lab, and wireless communication lab.

HOD-Incharge for ECE DepartmentFrom Jan 2013 to August 2013As HOD-Incharge organized a national level technical Symposium in the UAE and also activelyparticipated in promoting courses of the Department

Mobile No. 00971503875235

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Motorola Mobility India Pvt. Ltd	From Dec 2007 to Aug 2011		
Senior Patent Engineer			
Prosecution of already filed Patent Applications in the US and other countries			
Drafting of Patent Applications for new inventions in house			
Training and Mentoring of new hires.			
Siemens Corporate Technology, Bangalore	From Jan 2007 to June 2007		
Intern - Developed programs for DSP Hardware			
BITS,Pilani	From Jan 2005 to Dec 2006		
Faculty in the department of Electrical Electronics & Instrumentation gro	oup-		
Handled tutorials and Laboratories in the field of Digital signal processing, Communication systems, RF			
Microwave, Digital Electronics, Analog Electronics.			
SRM Valliammai Engineering College	From Jul 2002 to Dec 2004		
Lecturer in the department of Electronics and Communication. Also technical In-charge in setting up of new			
labs, namely Electronic system design lab, RF Lab in the college.			
Educational Qualification			
1. Post Graduation			
Masters in Business Law (Distance Education) - Presently Pursuing			

National Law School of India University (NLSIU), Bengaluru.

2. Post Graduate Technical Qualifica	tion	
Masters in Engineering		
Communication Engineering		
BITS, Pilani(Rajasthan)	Jan2005-Jun2007	8.85 (on a scale of 10)
3.Technical Qualification		
Bachelor of Engineering		
Electronics and Communication		
Madras University	Sep1997-May2001	77.83% (with distinction)

4. Senior Secondary level(Science	Stream)	
Tamil Nadu State Board -	1997	84.25%
5. Secondary level		
Tamil Nadu State Board -	1995	87.6%

IPR certifications

WIPO certification
General course on Intellectual Property
Introduction to the Patent Cooperation Treaty
IP Panorama certification as a part of Advanced International Certification Course conducted by KIPO, WIPO,
KAIST, and KIPA
NPTEL certification on Patent Drafting for Beginners

Databases Working Knowledge

Paid Database: 1. Derwent 2. PatBase 3. Patsnap 4. Patseer 5. OrbitOpen Database: 1. Google Patents 2. Free Patents Online

Personal Information

Date of Birth	28.08.1980
Languages known	Tamil, Hindi, English
Marital Status	Married

SANGEETHA