

# CURRICULUM VITAE

## PERSONAL INFORMATION

Name : JYOTISIKHA DEKA  
Date of Birth : 04.04.1986  
Gender : Female  
Marital Status : Married  
Nationality : Indian  
Caste : General  
Father's Name : Dr. Suresh Ch. Deka



Permanent Address : Ambikagiri Nagar, Right 1<sup>st</sup> Bye-lane,  
House No. 4, P.O. - Zoo Road,  
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Present Address : House No-142, 3<sup>rd</sup> Floor  
Block B, Lajpat Nagar 1  
New Delhi - 110024

## WORK EXPERIENCE:

NAME OF ORGANIZATION	DESIGNATION	EXPERIENCE	TYPE OF WORK/REMARK
Subramaniam and Associates (SNA), Gurgaon	Patent Associate	10.06.2019 till date	* Conducting Patent Search * Drafting Patent Specifications * Patent filing * Patent prosecution - preparing responses to Office Actions and attending Hearings
Technology Information Forecasting and Assessment Council (TIFAC)	Women Scientist 'C'	01.06.2018 to 31.05.2019	* Introductory course in IPR for 1 Month in TIFAC. * Practical Learning in Subramaniam and Associates (SNA) for 11 months.
Assam down town University	Assistant Professor (Electronics & Communication Engineering)	01.08.2012 to 31.01.2017	Teaching B. Tech students Engineering Tabulation

**ADDITIONAL INFORMATION:** Registered Patent Agent  
(IN/PA: 3700)

## ACADEMIC RECORD:

Degree	Qualification	Institute	Board / University	Year	Aggregate %
Post Graduation	M. Tech (Electronics Design & Technology)	Tezpur University	Tezpur University	2012	<b>86.50</b>
Graduation	B.E. (Electronics & Telecommunication Engineering)	Girijananda Chowdhury Institute of Management and Technology	Gauhati University	2010	<b>76.00</b>
12 <sup>th</sup>	Higher Secondary	Cotton College, Assam	Assam Higher Secondary Education Council	2005	<b>67.60</b>
10 <sup>th</sup>	High School Leaving Certificate (HSLC)	St. Mary's Convent, Guwahati, Assam	Board of Secondary Education, Assam	2003	<b>84.33</b>

## PUBLICATIONS:

- 1) **J. Deka** and S. Sharma, "A Novel Approach for modelling the Threshold Voltage and Sub threshold Swing of Nano Cylindrical MOSFET", *International Journal of Electronics and Applied Research (IJEAR)*, volume 1, pp 29-37, ISSN: 2395-0064, 2014
- 2) **J. Deka** and S. Sharma, "Modeling a Nano Cylindrical MOSFET Considering Parabolic Potential Well Approximation", **Springer India 2015**, *Advances in Communication and Computing*, Lecture Notes in Electrical Engineering, volume 347, pp 241 – 252, ISSN 1876-1100, ISSN 1876-1119 (electronic) DOI 10.1007/978-81-322-2464-8.

## DECLARATION:

I do hereby declare that the information furnished above is true to the best of my knowledge.

**Place:** New Delhi

Jyotisikha Deka