



UMESH KUMAR

IP PROFESSIONAL

PROFILE

I worked lastly in Epiphany IP as Sr. Associate in Drafting Team and Assistant Manager in Search Team. My educational background encompasses a Bachelor's and master's degree in Electronics Engineering (ECE). With a wealth of experience spanning nine years in the field of Intellectual Property Rights (IPR), I possess expertise in various areas, including patentability searching, invalidity/validity assessments, infringement (EOUs), Standard Essential Patents in the Telecom sector (SEPs), as well as drafting and handling office actions (prosecution). I am well-versed in utilizing IPR tools such as Orbit, Discover, Patbase, and TI. Prior to my engagement in the IPR field, I also served as an Assistant Professor in various educational institutions.

CONTACT

PHONE:
+918882616725
+919625293659

EMAIL:
umesh0532@gmail.com

ADDRESS:
C/o Pankaj, Near Shri Balaji Dham
mandir, Kaka Nagar, Shamli
Uttar Pradesh (247776)

HOBBIES

Guitar
Painting

EDUCATION

Delhi Technological University (Formerly DCE), Delhi
2011-2013

Master of Technology, VLSI Design and Embedded System
GPA 8.22/10 (First Division)

National Institute of Technology (NIT), Warangal
2005-2009

Bachelor of Technology, Electronics and Communication Engineering
GPA 6.64/10 (First Division)

WORK EXPERIENCE

Epiphany IP Solutions Pvt. Ltd as Sr. Associate

[July 2023 to June 2024]

Patentability searching in Hi-Tech Domain related to semiconductor memories, Patent drafting in AI/ML domain.

TT Consultants Ind Pvt Ltd as Team lead

[April 2022 to Jan 2023]

Preliminary Invalidity searching on current litigation APAC region clients.
Invalidity and Infringements

Researchwire Knowledge Solutions Pvt ltd as Team lead

[August 2021 to April 2022]

SEP (standard essential patents infringement charting in Telecom domain)
Invalidity and Infringement

LS Davar as Senior Patent Analyst

[February 2020 to March 2021]

Worked on Office Action, Invalidity, EOU, Patentability Searches and landscape projects, Patent Drafting.

Arctic Innovation India Pvt Limited as Senior Patent Engineer

[May 2017 to February 2020]

Worked on Office Action, Invalidity, EOU, Patentability Searches and landscape projects
Worked on IP brokering related projects

CPA Global Support Services as IP Researcher

[April 2014 to May 2017]

Worked on Invalidity, EOU, Patentability Searches

PUBLICATION SUMMARY

Patent and Research Papers

- "Complementary CNTFET based a novel ternary and quaternary logic generator on 32nm technology" (**Patent Appl. No. IN2690DE2013 A**)
- "**CMOS Body Driven Quaternary Logic Generator**" in International Journal of Science and Research with DOI "**10.9790/4200-0114650**".
- "**CNTFET Technology Based Precision Full-Wave Rectifier Using DDCC**" published in International Journal of Engineering and Science.
- "**Fiction Supporting Decelerated Expansion of the Universe**" in International Journal of Science and Research with DOI "**10.9790/4861-0214142**".
- "**Patch Antenna Array Fault Modelling and Its Monitoring**" in International Journal of Science and Research with DOI "**10.9790/2834-0340611**".

Personal Details

Father's Name	Vikrama Ram
Mother's Name	Usha Devi
Date of Birth	25/06/1987
Place of Residence	Ghazipur Uttar Pradesh India
Gender	Male
Marital Status	Married
Nationality	Indian
Language Proficiency	Hindi and English
Passport	Z4085408

Declaration

I, hereby declare that all the information furnished by me is true and fare to the best of my knowledge and I shall be solely responsible for all the declaration made above.

Signature

(Umesh Kumar)

Date