

Kamini
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Career Objective:

Looking for challenging opportunity to work as a patent agent at a leading Intellectual Property law Firm/organisation.

Experience Summary:

Approximately 5.0 years (August, 2019 –Present)

- Worked at “**Kan and Krishme**” as a **Patent Engineer** from November’ 2020 – till August 2022 and presently working as **Patent Associate**.
- Worked as an intern in IPR DIVISION, TIFAC, New Delhi from Aug’ 2019 – July’ 2020 under the women Scientist-C (WOS C-11) Scheme (also known as KIRAN-IPR) implemented by TIFAC, DST, New Delhi.
- Worked as an out-sourcing staff in IP Award Evaluation for three months, in Confederation of Indian Industry (CII) during on job training at IPR Division, TIFAC New Delhi.

Academic qualification and achievements:

- Qualified **Patent Agent Examination’ 2022** conducted by Intellectual Property Office, India.
- Completed one year training on IPR under the **KIRAN-IPR** Scheme implemented by **TIFAC, DST, New Delhi**.
- Completed M.Tech in Electronic Circuits and Systems from Integral University, Lucknow in 2019, scored 83.75% (CGPA: 9.03).
- Completed B.Tech in Electronics and Instrumentation from Gautam Buddh Technical University Lucknow in year 2010, scored 66.96%.
- Intermediate from CBSE Board, Sultanpur (UP) in the year 2004, scored 59.6%.
- High School from UP Board, Sultanpur (UP) in the year 2002, scored 65.6%.

Detailed Job Description:

- Organization: **Kan And Krishme**
Designation: **Patent Associate**
Duration: November’ 2020 – till present

Role (closely working with the in-house Attorneys): -

- Providing details analysis for First Examination report and how to overcome the raised objection of the Controller.
- Preparing final response of the First Examination report.
- Suggestion for amendments in the claims.
- Notifying formal requirements with relevant forms and their deadlines to the client.
- Preparing supporting documents and arguments for hearing
- Reporting to the client regarding hearing and giving detailed analysis of how to overcome the objections.

- Preparing hearing response and submission.
- Solving queries of the client.

Additionally (for design Applications): -

- Providing details analysis for First Examination report and how to overcome the raised objection of the Controller (response to client) for Design applications.
- Preparing final response of the Examination report (to the controller).
- Prepared response for the queries of the client closely working with the in-house Attorneys.

- Organization: **IPR Division, TIFAC, New Delhi**

Designation: Women Scientist-C (Under KIRAN IPR Scheme)

Duration: August, 2019 - July, 2020

Role: -

- Prepared landscape Report, technology scan report, prior art search and Patentability Search Report using search engine: Patent scope, EPO, USPTO, JPO, KIPO, Google Patents, Google scholar, Web of science.
- completed a general course on Intellectual property organised by WIPO academy from November 11, 2019 to January 19, 2020, during IPR training

Research Project Details:

Project: “Current mirror integrator & Analog signal processing”

Project description: This research project has been carried out as part of the Dissertation during M. Tech (Final year). Objective of this research is to provide simple and small electronic circuit designs for portable electronic devices operating at a very low voltage with minimal power dissipation. In this regard, NMOS Current mirror is used for the designing of the different devices of the Analog signal processing like current amplifier, Integrator, Differentiator and low pass filter using the CM integrator, and the obtained results are similar and identical to the theoretical result and values of the respective conventional circuits.

Simulation Tool used: HSPICE software of CMOS 45nm Technology.

Project delivered in college:

- “Temperature indicator” using AT 89C52 microcontroller (B.tech Project).
- Miniproject on “multimelody generator” using UM 3481A/WR 630173.
- Design PCB by using software Express PCB.

Research Paper Published:

- July 2019, “Analog Signal Processing Applications of Current Mirror Amplifier: A Circuit Design Perspective”, International Journal of Advance Research in Science and Engineering, Volume No. 08, Issue No. 07.

Achievements:

- Got Scholarship (1 year) in Intellectual Property Right Management (IPR) from DST (Department of Science and Technology) by Govt. of India from TIFAC under KIRAN-IPR, WOS-C scheme, 11th Batch.

- Attended 2nd national workshop on "Advances in wireless optical networks" jointly organized by department of electronics and communication Engineering, MNNIT Allahabad and KNIT Sultanpur held at MNNIT ALLAHABAD, Prayagraaj during May 27th to June 1st, 2019.

Personal details:

Father's Name	Dr. C. L. Yadav
Marital Status	Single
Nationality	Indian
Languages Known	English, Hindi
Permanent Address	647F/495, shiv vihar, sec-I, Jankipuram Lucknow-226021.

DATE:-

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